

L Number	Hits	Search Text	DB	Time stamp
2	14	control same direction same traffic same impedance	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/09 17:18
3	10	4357658.uref.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/09 17:17
4	3336	control same direction same impedance	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/09 17:50
5	2	(control same direction same impedance) same uart	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/09 17:22
6	0	6453374.uref.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/09 17:22
7	1		USPAT	2004/06/09 17:22
8	0	(control same direction same impedance) same instataneous	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/09 17:23
9	807	(control same direction same impedance) same source	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/09 17:24
10	201	(control same direction same impedance) same \$processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/09 17:36
11	19	(control same direction same impedance) same master same slave	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/09 17:40
12	1780	(control same direction same impedance) same circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/09 17:40
13	102	((control same direction same impedance) same circuit) same memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/09 17:40
14	0	half same duplex same direction same impedance	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/09 17:50
15	18	half same duplex same direction same impedance	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/09 17:55

16	2	4831651.uref.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/09 17:54
17	1	spi same direction same impedance	USPÄT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/09 17:55
18	42	spi same impedance	USPÄT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/09 17:55